

UTILITY PATENT APPLICATION TRANSMITTAL

(Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.

JEL 31088

Total Pages in this Submission

3

TO THE ASSISTANT COMMISSIONER FOR PATENTS

Box Patent Application

Washington, D.C. 20231

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

IMAGE PROCESSING METHOD

and invented by:

Hiroyuki MORIMATSU

If a **CONTINUATION APPLICATION**, check appropriate box and supply the requisite information:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

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Enclosed are:

Application Elements

1. ☒ Filing fee as calculated and transmitted as described below
2. ☒ Specification having 14 pages and including the following:
 - a. ☒ Descriptive Title of the Invention
 - b. ☐ Cross References to Related Applications (if applicable)
 - c. ☐ Statement Regarding Federally-sponsored Research/Development (if applicable)
 - d. ☐ Reference to Microfiche Appendix (if applicable)
 - e. ☒ Background of the Invention
 - f. ☒ Brief Summary of the Invention
 - g. ☒ Brief Description of the Drawings (if drawings filed)
 - h. ☒ Detailed Description
 - i. ☒ Claim(s) as Classified Below
 - j. ☒ Abstract of the Disclosure

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Application Elements (Continued)

3. ☒ Drawing(s) (when necessary as prescribed by 35 USC 113)
- a. ☒ Formal Number of Sheets 5
- b. ☐ Informal Number of Sheets _____
4. ☒ Oath or Declaration
- a. ☒ Newly executed (original or copy) ☐ Unexecuted
- b. ☐ Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional application only)
- c. ☒ With Power of Attorney ☐ Without Power of Attorney
- d. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application,
see 37 C.F.R. 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation By Reference (usable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied
under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby
incorporated by reference therein.
6. ☐ Computer Program in Microfiche (Appendix)
7. ☐ Nucleotide and/or Amino Acid Sequence Submission (if applicable, all must be included)
- a. ☐ Paper Copy
- b. ☐ Computer Readable Copy (identical to computer copy)
- c. ☐ Statement Verifying Identical Paper and Computer Readable Copy

Accompanying Application Parts

8. ☒ Assignment Papers (cover sheet & document(s))
9. ☐ 37 CFR 3.73(B) Statement (when there is an assignee)
10. ☐ English Translation Document (if applicable)
11. ☒ Information Disclosure Statement/PTO-1449 ☒ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Acknowledgment postcard
14. ☐ Certificate of Mailing
- ☐ First Class ☐ Express Mail (Specify Label No.): _____

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Accompanying Application Parts (Continued)

15. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)

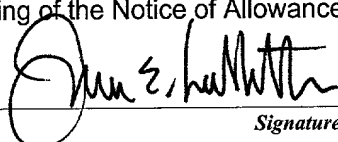
16. ☐ Additional Enclosures (please identify below):

Fee Calculation and Transmittal

CLAIMS AS FILED

For	#Filed	#Allowed	#Extra	Rate	Fee
Total Claims	4	- 20 =	0	x \$18.00	\$0.00
Indep. Claims	4	- 3 =	1	x \$78.00	\$78.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
BASIC FEE					\$690.00
OTHER FEE (specify purpose)					\$0.00
TOTAL FILING FEE					\$768.00

- ☒ A check in the amount of \$768.00 to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 19-4375 as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of as filing fee.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.
- ☐ Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.311(b).


Signature

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Dated: May 30, 2000

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- 1 -

IMAGE PROCESSING METHOD

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to an image processing method and an image processing apparatus used with a printer, a scanner, a duplicator, a facsimile, etc. for reproducing the multivalued image information as a binary image.

Description of the Related Art

The error spreading method is widely known as one of the methods for converting a multivalued image into a binary image.

Fig. 5 is a block diagram showing a conventional circuit for executing the error spreading method.

In Fig. 5, the multivalued data D of an intended pixel to be binarized is read from an image memory 100, and subjected to γ correction into the multivalued data corresponding to the printing characteristics of an output device such as a printer with reference to the correction data stored in a γ correction ROM 101. The error data E of the intended pixel is added to the multivalued data D' subjected to γ correction by an adder 102 of an error spread processing unit 107, so that $F = D' + E$ is output.

5 in the case where $F < Th$, a binary signal $B = "0"$ is
output. From this output result, the binarization error
 E' is calculated as $E' = F - B'$ by a subtractor 106.

10 multivalued data D is 230 and the binarization threshold value Th is 128, for example, the output data after binarization is B = 1, so that the binarization error E' is given as $E' = D - B \times 255 = 230 - 1 \times 255 = -25$.

15 memory 103 in order to distribute it among the data of
the pixels subsequently processed in accordance with a
predetermined error matrix M_{xy} in a weighted error
calculator 105, and added to the multivalued data of the
next pixel by an adder 102, thereby transmitting the
20 error data.

25 corresponding to 255 out of 256 tones. Thus, an error of
25 develops with respect to 230 of the input multivalued
data D. The error 25 with respect to the input
multivalued data D of 230 is determined as a binarization

5 The error matrix used in the conventional error
spreading method is shown in Fig. 6.

The error generated in binarizing this intended pixel is distributed to the unprocessed the next pixels with the weighting coefficients (7, 1, 5, 3) shown in Fig. 6. In binarizing the intended pixel indicated by *, the error distribution value stored is read from the error memory 103, and the next input value read from the image memory 100 is corrected using this error distribution value.

The image binarized by the error spreading method poses the problem of the reproducibility of the edge portion of the binarized image due to the fact that the error is distributed to the surrounding pixels. In other words, since the information of the surrounding pixels is partly added to the intended pixel, the

The conventional approach to this problem is a method of improving the edge retention by emphasizing the edge portion of the original multivalued data through a high-pass filter or the like.

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SUMMARY OF THE INVENTION

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In the case where the density difference (edge intensity) is large between the intended pixel and the

adjacent pixels, the binarization threshold is set to a low value to increase the probability of dot generation, and otherwise the binarization threshold is set to a high value to decrease the probability of dot generation.

- 5 Thus, the edge reproducibility of the binary image after the error spreading process can be improved.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram showing a circuit for executing an image processing method according to an
10 embodiment of the invention.

Fig. 2 is a diagram for explaining the pixels detected as an edge portion in an image processing method according to an embodiment of the invention.

Figs. 3A, 3B, 3C are diagrams for explaining
15 the setting of a threshold value for the binarization process in an image processing method according to an embodiment of the invention.

Fig. 4 is a flowchart showing the processing steps in an image processing method according to an
20 embodiment of the invention.

Fig. 5 is a block diagram showing a circuit for executing the conventional error spreading method.

Fig. 6 is a diagram for explaining an error matrix used in the conventional error spreading method.

25 DESCRIPTION OF THE EMBODIMENTS

An embodiment of the present invention will be

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right pixels of the intended pixel 120, the density is referenced at the time of edge detection.

Now, assuming that the intended pixel is 120, an explanation will be given of the detection as to whether there is any edge portion adjacent to this intended pixel. Assume that the density data of the intended pixel 120 is $D1$, the density data of the adjacent pixel 121 is $D2$, the density data of the adjacent pixel 122 is $D3$, and the density difference of the intended pixel 120 with the pixels 121 and 122 adjacent to the left and right sides thereof are DL and DR , respectively. The density difference of the intended pixel 120 with the left adjacent pixel 121 and the right adjacent pixel 122 are given as $DL = |D1 - D2|$ and $DR = |D1 - D3|$, respectively.

In the case where this value exceeds a predetermined value S , the intended pixel 120 is detected as an edge portion. In other words, in the case where the relation $DL > S$ or $DR > S$ holds, the particular intended pixel is determined as an edge portion.

Although the left and right adjacent pixels of the intended pixel are assumed to be adjacent pixels in this embodiment, the density of the upper and lower adjacent pixels or all the adjacent pixels including the left and right adjacent pixels or the surrounding pixels can be referenced to improve the edge detection accuracy and perform the control operation in accordance with the edge position, thus further improving the edge

reproducibility.

Now, an explanation will be given of the operation of controlling the dot generation upon detection of left and right adjacent edges of the
5 intended pixel.

With the pixel of which the left or right adjacent edge has been detected, the edge retaining process is executed by controlling the dot generation in accordance with the density difference between the
10 intended pixel and the left and right adjacent pixels. In this way, the reproducibility of the left and right adjacent edges can be improved. In controlling the dot generation, the rate of dot generation can be reduced by changing the threshold for binarization of the pixel of
15 which left and right adjacent edges have been detected.

This process will be explained with reference to Figs. 3A to 3C showing the relation of the dot on/off with respect to the setting of the density and the threshold of the image data for the error spread
20 operation.

Generally, the binarization threshold in the conventional error spreading method, as shown in Fig. 3A, is set and fixed at about 128 intermediate of the input density of 256 tones. According to this embodiment, on
25 the other hand, as shown in Fig. 3B, the dot off area is widened by increasing the binarization threshold for the pixel of which left and right adjacent edges have been detected. In this way, the dot generation is suppressed

result that dots are not easily generated. For the pixels not determined as an edge portion, the binarization process is executed with a fixed threshold Th of 128.

5 According to this embodiment, the threshold Th is controlled by the aforementioned formula. A more faithful edge reproduction is possible, however, by controlling the threshold Th using a formula having a nonlinear characteristic without resorting to the formula
10 having the aforementioned linear characteristic.

Without using a calculation formula, a more detailed setting of a threshold is made possible by preparing a table for holding threshold values corresponding to the density difference D between the
15 intended pixel and the adjacent pixels, and setting a threshold value by reference to this table.

This binarization process will be explained with reference to the flowchart of Fig. 4.

In Fig. 4, first, a line of multivalued data of
20 the image to be binarized is stored in the image memory 100 (step s200), and then the error data distributed to the pixels on this line are stored in the error memory 103 (step s210). The density data D1 of the pixel to be binarized from a line of the data is read and a weighted
25 error in this pixel is added thereby to acquire the intended pixel data. At the same time, this pixel data is γ -corrected based on the correction data in the γ -correction ROM 101 (step s220).

Then, in the adjacent edge detector 108, the density data D2, D3 of the left and right adjacent pixels of the intended pixel are acquired from the image memory 100 (step s230), thereby calculating the density difference $DL = |D1 - D2|$ and $DR = |D1 - D3|$ between the density data D1 of the intended pixel and the density data D2 and D3 of the left and right pixels (step s240), respectively.

The density differences DL, DR thus obtained are compared with a setting S (step s250), and in the case where the density difference DL or DR is larger than the setting S, the threshold determining unit 109 detects this pixel as the one adjacent to the edge, and sets the threshold Th using the formula $Th = 128 - D/N$ in accordance with the edge intensity (step s270). In the case where both the density differences DL and DR are smaller than the setting S, on the other hand, this pixel is determined as the one not adjacent to an edge and the threshold 128 is maintained (step s260).

After setting a threshold in this way, this intended pixel is binarized by error spreading (step s280), and thus the process for this pixel is terminated.

It is then determined whether the aforementioned process has been completed for all the pixels on the current line (step s290), and unless the process is not complete for all the pixels of the line, the process proceeds to the next pixel (step s310) for executing steps s220 to s280. In the case where the

process for all the pixels on the line is complete, on the other hand, it is determined whether the process is complete for all the lines (step s300). Unless the process for all the lines is complete, the operation
5 proceeds to the next line (step s320), and the same operation is repeated until the process for all the lines is completed.

As described above, according to this embodiment, the density difference between the intended pixel and adjacent pixels is determined, and in the case where the density difference is larger than a predetermined setting, the intended pixel is determined as an edge portion. In such a case, the threshold for binarizing the intended pixel constituting an edge portion is set to a value which is smaller, the larger the edge intensity with the adjacent pixels. As a result of this binarization, in the case where the edge intensity with the pixel adjacent to the intended pixel is large, the binarization threshold is set to a low value to increase the probability of dot generation. Otherwise, the binarization threshold is set to a high value for a lower probability of dot generation. As a result, the edge reproducibility of the binary image after the error spreading can be improved.

25 The thresholds used in this embodiment are only
an example of numerical values that can be employed, and
the invention is not limited to these numerical values.

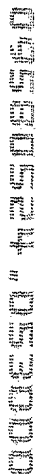
It will thus be understood from the foregoing

[illegible]

setting the binarization threshold for said intended pixel determined as an edge portion to a value lower, the larger the edge intensity with the adjacent pixel and higher, the smaller the edge intensity with the adjacent pixel.

means for setting the binarization threshold of said intended pixel in accordance with said density difference and binarizing said intended pixel.

THE UNIVERSITY OF CHICAGO



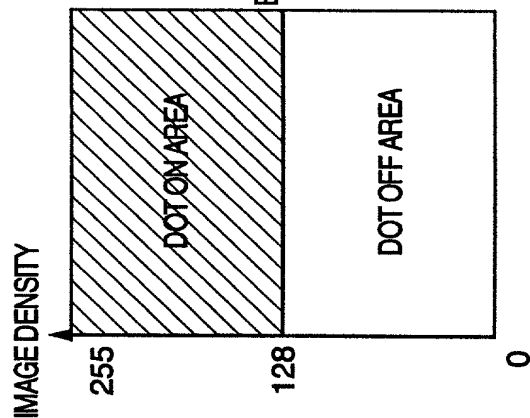


FIG.3A

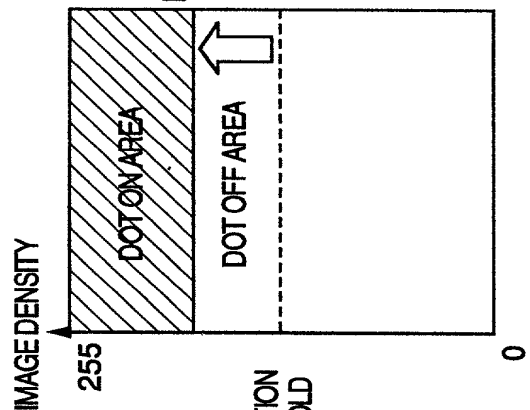


FIG.3B

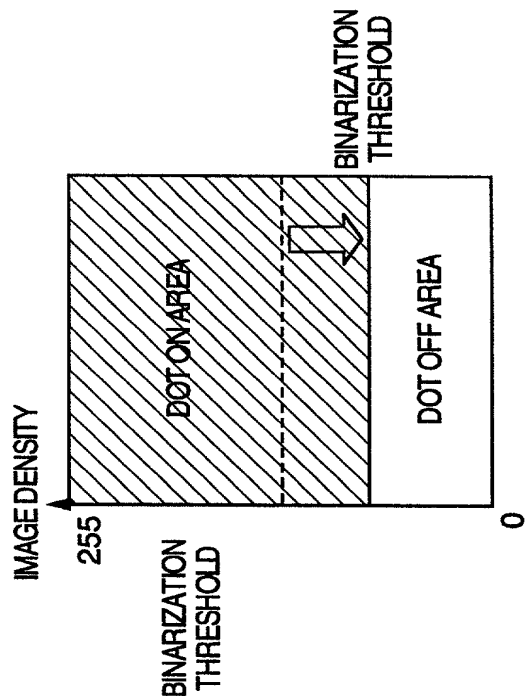
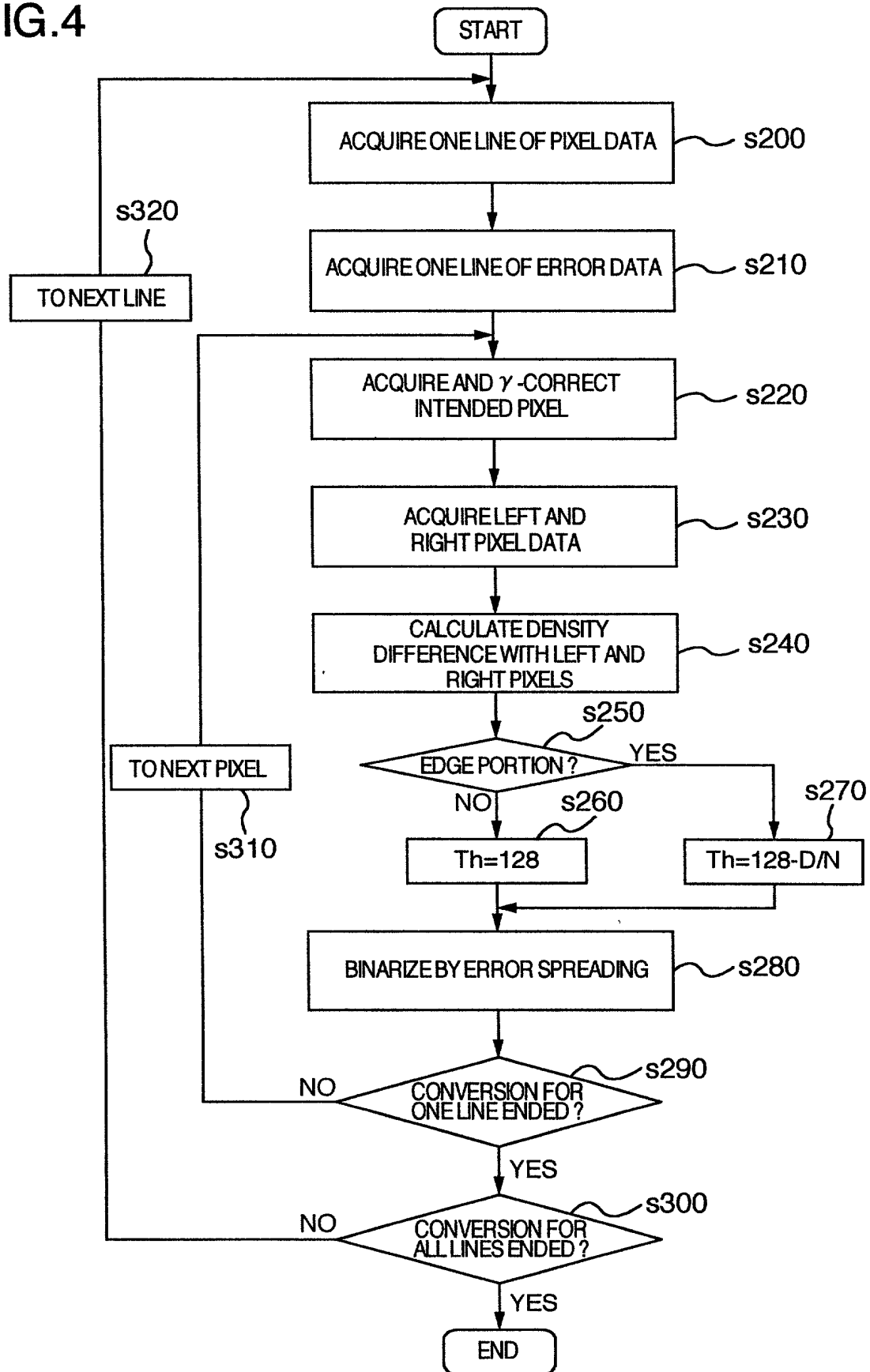


FIG.3C

FIG.4



Declaration for Patent Application

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on

the invention entitled: "IMAGE PROCESSING METHOD"

the specification of which

2 (file no. _____)

(check at least one)

3 ☒ is attached hereto

4 ☐ was filed on _____ as (5) U.S. Application Serial No. _____

6 ☐ and was amended on _____

(if applicable)

Use this portion only if you are entering the U.S. National phase based on a PCT International Application designating the U.S.

7 ☐ was filed as PCT international application

8 Number _____

9 on _____

and was amended under PCT Article(s) 19 and/or 34

10 on _____ (if applicable).

11 priority date claimed in PCT International Application

(Country)

(Number)

(Day/Month/Year Filed)

I hereby declare that I have reviewed and understand the contents of the above identified specification, including the claims, as amended, by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me which is material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date earlier than that of the application(s) on which priority is claimed:

Prior (Foreign) Application(s) and Priority Claims Under 35 U.S.C. 119

Priority Claimed

Japan 11-163286 10 June, 1999
(Country) (Number) (Day/Month/Year Filed)

☒ Yes ☐ No

(Country) (Number) (Day/Month/Year Filed)

☐ Yes ☐ No

Do not use this portion to identify a PCT application if the present application is the U.S. National phase of that PCT application

13

I hereby claim the benefit under Title 35, United States Code, 120 of any United States application(s) or PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between filing date of the prior application and the national or PCT international filing date of this application.

(U.S. Application Number)

(U.S. Filing Date)

Status (patented, pending, abandoned)

I hereby appoint the following attorneys of the firm of STEVENS, DAVIS, MILLER & MOSHER, L.L.P. as my attorneys of record with full power of substitution and revocation to prosecute this application and to transact all business in the Patent and Trademark Office:

James E. Ledbetter, Reg. No. 28732; Thomas P. Pavelko, Reg. No. 31689; and Anthony P. Venturino, Reg. No. 31674.

ALL CORRESPONDENCE IN CONNECTION WITH THIS APPLICATION SHOULD BE SENT TO STEVENS, DAVIS, MILLER & MOSHER, L.L.P., 1615 L Street, N.W., Suite 850, Washington, D.C. 20036; Mailing Address: P.O. Box 34387, Washington, D.C. 20043, TELEPHONE (202) 408-5100 FACSIMILE (202) 408-5200 or (202) 408-5088.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful statements may jeopardize the validity of the application or any patent issuing thereon.

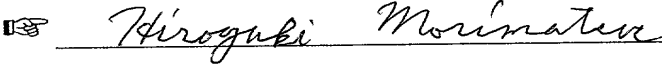





See page 2 for signature lines

See instructions for completing this form

00050-1250850

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PAGE 2 OF U.S.A. DECLARATION FORM

*14a	Typewritten Full Name of Sole or First Inventor	<u>Hiroyuki MORIMATSU</u>		
		Given Name	Middle Name	Family Name
*15a	Inventor's Signature			
*16a	Date of Signature	 <u>May 9, 2000</u>		
		Month	Day	Year
17a	Residence	<u>Kurume-shi, Japan</u>		
		City	State or Province	Country
18a	Citizenship	<u>Japan</u>		
19a	Post Office Address (Insert complete mailing address, including country)	<u>284-2, Minamimachi, Kurume-shi, Japan.</u>		
*14b	Typewritten Full Name of 2nd Inventor (if any)			
		Given Name	Middle Name	Family Name
*15b	Inventor's Signature			
*16b	Date of Signature			
		Month	Day	Year
17b	Residence			
		City	State or Province	Country
18b	Citizenship			
19b	Post Office Address (Insert complete mailing address, including country)			
*14c	Typewritten Full Name of 3rd Inventor (if any)			
		Given Name	Middle Name	Family Name
*15c	Inventor's Signature			
*16c	Date of Signature			
		Month	Day	Year
17c	Residence			
		City	State or Province	Country
18c	Citizenship			
19c	Post Office Address (Insert complete mailing address, including country)			

*Note to Inventor: Please sign name on line 15 exactly as it appears in line 14 and insert the actual date of signing on line 16. If there are more than three inventors, please add a copy of this page for identification and signatures for the additional inventors.